




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Substitute for form 1449A/B/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)			<b>Complete if Known</b>		
			Application Number	Not Yet Assigned 10/7/2004	
			Filing Date	Concurrently Herewith	
			First Named Inventor	Robert C. Taft	
			Art Unit	N/A	
			Examiner Name	Not Yet Assigned	
Sheet	1	of	1	Attorney Docket Number	08211/0200252-US0

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number Number-Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>

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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	CA	TAFT et al., "A 100-MS/s 8-b CMOS Subranging ADC with Sustained Parametric Performance from 3.8V Down to 2.2V", <i>IEEE Journal of Solid-State Circuits</i> , March 2001, pp. 331-338, Volume 36 No. 3	
	CB	PAN et al., "A 3.3V, 12b 50MSample/s A/D Converter in 0.6 um CMOS with over 80dB SFDR", <i>2000 IEEE Journal of Solid-State Circuits Conference</i> , February 7, 2000, pp. 40-41, Session 2	
	CC	SONG, "Design of Nyquist-Rate ADC", Slide Show, October 10, 2003, pp 1, 22 and 24	

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Examiner Signature		Date Considered	10-19-04
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